

AMENDMENTS TO THE CLAIMS

Please amend the claims as follows:

1. (Currently Amended) A computer system comprising:  
~~a first video controller integrated into a chipset including a bus-IO controller, a memory controller and a first video controller;~~  
a second video controller coupled to the chipset;  
~~a first at least one display device coupled to the video controllers; a second display device;~~  
a switching device configured to receive different analog and digital signals from the video controllers at respective inputs and to provide each of the signals to a compatible one of the display devices device;  
~~the switching device being coupled to a first connector configured to receive the first display device; and~~  
the switching device being coupled to a second respective connector configured to receive the second display device, for each display device; and  
wherein the chipset includes an AGP port, and wherein the second video controller is configured to provide a compatible signal to the switching device using the AGP port.
2. (Cancelled).
3. (Cancelled).
4. (Cancelled)
5. (Cancelled).

6. (Previously Amended) The computer system of claim 1, further comprising:  
a processor coupled to the chipset; and  
a system memory configured to store a program that is executable by the processor;  
wherein the program includes instructions for causing the switching device to provide the first signal or the second signal to the first display device.
7. (Cancelled).
8. (Cancelled).
9. (Previously Amended) The computer system of claim 1, wherein the signals include analog and digital signals.
10. (Currently Amended) A computer system comprising:  
~~a first video controller integrated into a chipset including a bus-I/O controller, a memory controller and a first video controller;~~  
an interface coupled to the chipset and configured to receive a second video controller;  
~~a first and a second at least one display device coupled to the video controllers;~~  
a switching device coupled to receive different analog and digital signals from the video controllers at respective inputs and to provide each of the signals to a compatible ~~one of the display devices~~ device;  
~~the switching device being coupled to a first connector configured to receive the first display device; and~~  
the switching device being coupled to a second respective connector configured to receive the second display device. for each display device; and

wherein the chipset includes an AGP port, and wherein the AGP port is configured to receive a compatible signal from the second video controller.

11. (Cancelled).
12. (Cancelled).
13. (Cancelled).
14. (Cancelled).
15. (Previously Amended) The computer system of claim 10, further comprising:  
a processor coupled to the chipset; and  
a system memory configured to store a program that is executable by the processor;  
wherein the program includes instructions for causing the switching device to provide the first signal from the first video controller to the first display device in response to the second video controller not being coupled to the interface, and wherein the program includes instructions for causing the switching device to provide the second signal from the second video controller to the first display device in response to the second video controller being coupled to the interface.
16. (Cancelled).
17. (Cancelled).
18. (Previously Amended) The computer system of claim 10, wherein the signals include analog and digital signals.

19. (Cancelled).
20. (Cancelled).
21. (Currently Amended) A method of providing a video signal to a display device in a scalable platform comprising:
- ~~providing a first video controller integrated into a chipset including a bus-I/O controller, a memory controller and a first video controller;~~
- ~~providing a second video controller coupled to the chipset;~~
- ~~providing a first and a second at least one display device coupled to the video controllers; configuring an interface coupled to the chipset to receive a second video controller;~~
- ~~configuring the a switching device to receive different analog and digital signals at respective inputs from the video controllers and to provide each of the signals to a compatible one of the display devices. device; and~~
- ~~wherein the chipset includes an AGP port and wherein the second video controller is configured to provide a compatible signal to the switching device using the AGP port.~~
- E1  
Amend*